

憲法法庭 公告

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公告事項：

憲法法庭審理會台字第9433號等聲請案，業經辯論終結，依憲法訴訟法第26條第2項後段規定，延長宣示判決之期間。宣示判決日期另行公告。

憲法法庭
審判長 許宗力

Figure 1: A schematic diagram of a neural network architecture. The diagram shows a sequence of layers: an input layer, followed by a hidden layer, and an output layer. The input layer is connected to the hidden layer, which is in turn connected to the output layer. The diagram also shows the flow of information from the input layer to the hidden layer, and from the hidden layer to the output layer. The diagram is labeled with 'Figure 1' and 'A schematic diagram of a neural network architecture'.

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